

Attorney Docket No.: CYPR-CD00232

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Patent Application

Inventor(s): Snyder

Group Art Unit: 2183

Filed: October 22, 2001

Examiner: PAN, Daniel H.

Application No.: 10/033,027

Confirmation No.: 8635

Title: PROGRAMMABLE SYSTEM ON A CHIP

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<i>h</i>	A	5,399,922	3/21/95	Kiani et al.	326	40	7/2/93
<i>h</i>	B	5,680,070	10/21/97	Anderson et al.	327	336	2/5/96
<i>h</i>	C						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
<i>h</i>	L	0450863A2	10/9/91	EPO	G11C	27/02		
<i>h</i>	M	0499383A2	8/19/92	EPO	H03K	19/177		
<i>h</i>	N	0308583 A2	3/29/89	EPO	G06J	1/00		
<i>h</i>	O	069816A2	2/22/95	EPO	G06J	1/00		
<i>h</i>	P	1170671A1	1/8/02	EPO	G06F	17/13		
<i>h</i>	Q	95/32478	11/30/95	PCT	G06F	17/50		

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
<i>h</i>	R	Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing Cleveland, OH, US vol. 42, no. 21, 10/14/94, pages 63-64, 66,68,7 XP000477345 ISSN: 0013-4872 the whole document.
<i>h</i>	S	Harbaum T. El. Al: "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999, pages 335-342, XP000879556, page 337, right-hand column, line 13-page 338, left hand column, line 4; figure 1.
Examiner <i>h</i>		Date Considered <i>h</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.